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Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMERK OFFICE					ATTORNEY DOCKET NO. PD- 00-1019	SERIAL	NO.	0	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					APPLICANT Timothy A. Let		*	<u>a</u>	804 804 804
(Use of several sheets if necessary)					FILING DATE	GROUP	<u> </u>	6 U.S	<u> </u>
				U.S. PATENT	DOCUMENTS		•	103	
*EXAMINER INITIAL	NO.	DOCUMENT NUMBER	DATE	NAME		CLASS	SUBCLASS	GROU	,
TMA	1	6,018,806	1/25/00	Cortopassi, et al.		714	8		
TMB	2	5,805,882	9/8/98	Cooper, et al.		713	2		
TMB	3	5,398,333	3/14/95	Schieve, et al.		714	36		
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FOREIGN PATENT DOCUMENTS									
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					, Title, Date, Pertinent Pages, Etc.)				
	4	Intel Application Note AP-636, entitled "Preventing BIOS Failures Using Intel Boot Block Flash Memory," page 6.							
	5	Phoenix Developer's Reference for Phoenix BIOS 4.0, Release 6.0, at page 279.							
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* EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant

EXAMINER ...

DATE CONSIDERED